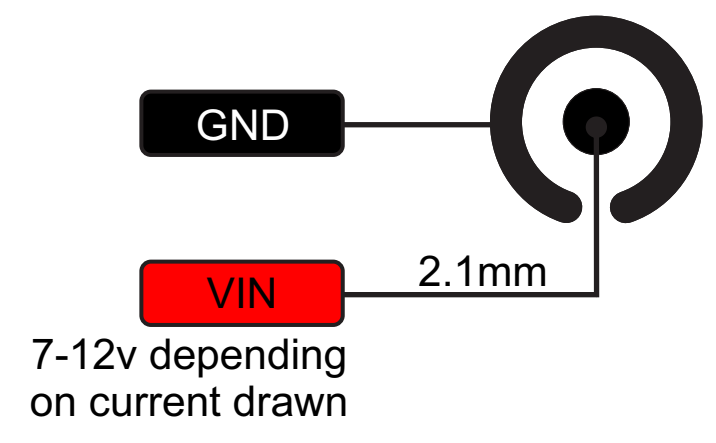


THE UNOFFICIAL  
**ARDUINO DUE**  
 PINOUT DIAGRAM

**LEGEND**

- GND
- POWER
- CONTROL
- PHYSICAL PIN
- PORT PIN
- SAM3X8E PIN FUNC
- DIGITAL PIN
- ANALOG-RELATED PIN
- PWM PIN
- SERIAL PIN

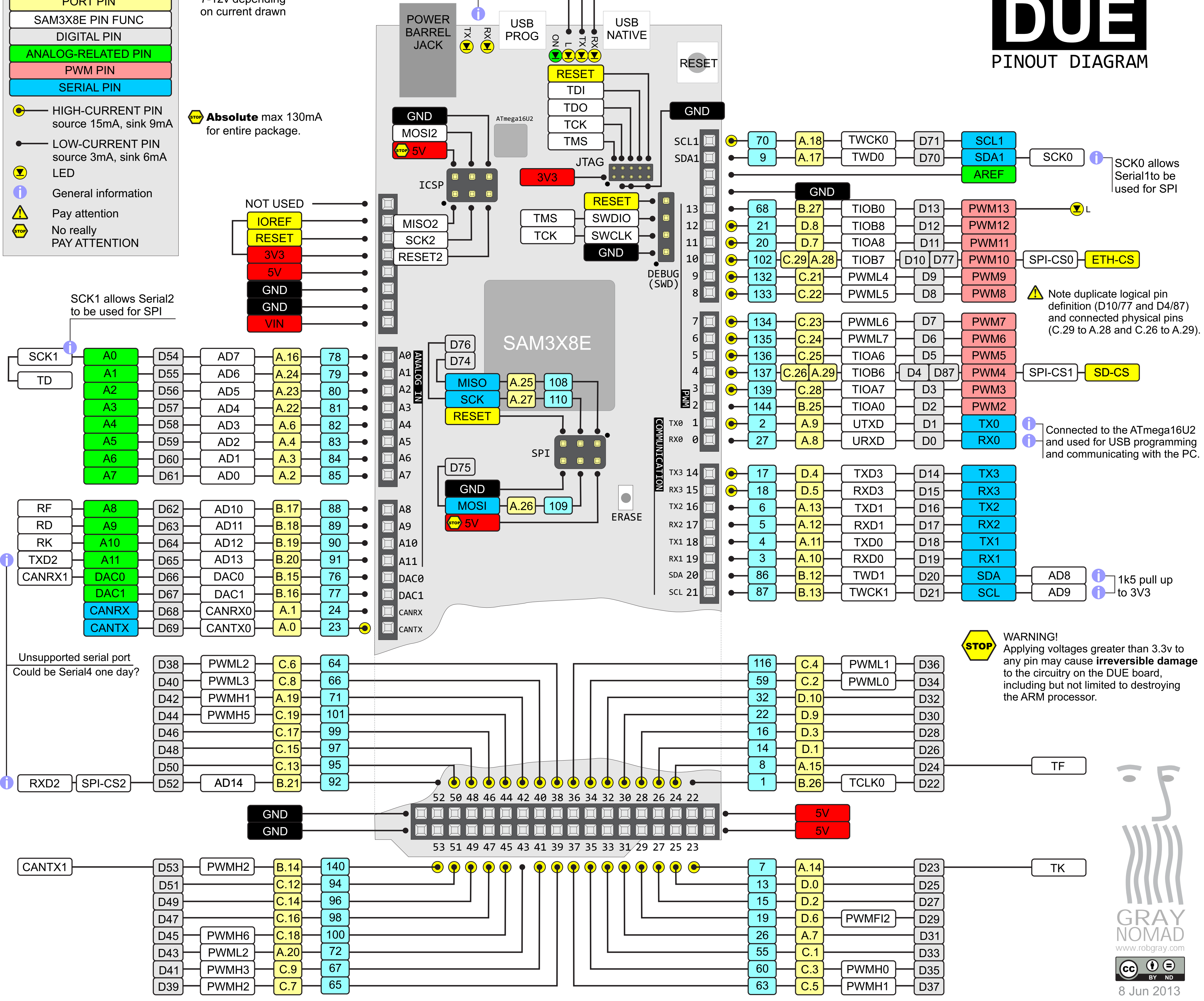
- HIGH-CURRENT PIN source 15mA, sink 9mA
- LOW-CURRENT PIN source 3mA, sink 6mA
- LED
- General information
- Pay attention
- No really PAY ATTENTION



Controlled by the 16U2, not accessible to the Due application software

Negative logic used for D73 and D72 when driving the LEDs, IE. LOW == ON

**Absolute** max 130mA for entire package.



Note duplicate logical pin definition (D10/77 and D4/87) and connected physical pins (C.29 to A.28 and C.26 to A.29).

**WARNING!** Applying voltages greater than 3.3v to any pin may cause **irreversible damage** to the circuitry on the DUE board, including but not limited to destroying the ARM processor.

